



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RE APPLICATION OF:

Yasuyoshi ITOH, et al.

GROUP: 2826

SERIAL NO: 10/600,344

FILED: June 23, 2003

EXAMINER: TRAN, TAN N.

FOR: SEMICONDUCTOR DEVICE WITH A METAL INSULATOR
SEMICONDUCTOR TRANSISTOR

SUBMISSION OF SUPPLEMENTAL APPLICATION DATA SHEET

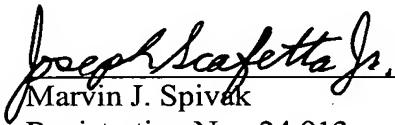
Office of Initial Patent Examination
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant(s) submit herewith a Supplemental Application Data Sheet for the above-identified application for the purpose of correcting the filing date of the 2nd priority document.

Respectfully Submitted,

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